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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/706,086	11/13/2003	Sadao Kotera	36856.1140	7271
75	90 02/09/20		EXAMINER	
Keating & Bennett LLP			NGUYEN, KHANH V	
Suite 312 10400 Eaton Pla	ace		ART UNIT	PAPER NUMBER
Fairfax, VA 22030			2817	
			DATE MAILED: 02/09/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/706,086	KOTERA ET AL.				
Office Action Summary	Examiner	Art Unit				
	Khanh V. Nguyen	2817				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tin within the statutory minimum of thirty (30) day rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed rs will be considered timely. I the mailing date of this communication. CD (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on 13 No.	ovember 2003.					
2a) ☐ This action is FINAL . 2b) ☒ This	☐ This action is FINAL. 2b) ☑ This action is non-final.					
• •	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4)⊠ Claim(s) <u>1-11</u> is/are pending in the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.					
	5) ☐ Claim(s) is/are allowed. 5) ☑ Claim(s) <u>1-11</u> is/are rejected.					
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examine	r					
)⊠ The drawing(s) filed on <u>13 November 2003</u> is/are: a)□ accepted or b)⊠ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a)⊠ All b)□ Some * c)□ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No.						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau		<u> </u>				
* See the attached detailed Office action for a list	of the certified copies not receive	ed.				
Attachment(s)	_					
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) 🔲 Interview Summary Paper No(s)/Mail Da					
 Notice of Draisperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 11/13/03. 		Patent Application (PTO-152)				

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DETAILED ACTION

Drawings

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the "an input tuning circuit" must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

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The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 1 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

It is not clear which "input tuning circuit" is intended.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 3-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Kunishima (6,037,999).

Regarding claim 1, Kunishima (Figs. 1, 2) discloses a tuner comprising: an input (11); a variable gain amplifier (4); a pre-amplifier (2); and a variable attenuator (1) including PIN diode (1a, 1b), wherein the structure recited in Kunishima (Fig. 1) is identical to that of the applicant's claims. As such, the claimed functions are presumed to be inherent.

Regarding claims 3, 4, wherein the AGC voltage can be set as recited in the claim (see col. 3, lines 61-67 and col. 5, lines 24-32).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kunishima.

Regarding 2, Kunishima discloses the claimed invention except the amount of DC flow through the PIN diode. However, such a current can be obtained can be set/controlled by AGC voltage via control voltage generating circuit (10) (see column 4, lines 21-34).

Claims 5-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kunishima in view Boulic (6,081,151).

Kunishima discloses the claimed invention except the variable attenuator having at least three PIN diodes.

Boulic (Figs. 1, 3) discloses an electrically controlled variable attenuator comprising at least three PIN diodes (D10-D12).

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Accordingly, it would have been obvious in view of the reference, taken as a whole, to have modified the attenuator circuit of Kunishima to have included an attenuator circuit as taught by Boulic since these two circuits are equivalent. As such, replacing one for another would be within the level of ordinary skill in the art. Regarding claim 6, wherein the diodes of Boulic are arranged as Pi-shaped attenuation circuit (see col. 1, lines 6-39).

Regarding claim 7, wherein Boulic (Fig. 1) discloses a choke coil (L10), a plurality of resistors (R10-R13), a plurality of capacitors (C10-C14), and wherein control voltage (Vc) is coupled to a transistor as shown in Figure 3.

Claims 7-11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kunishima in view Aoki et al. (4,369,414).

Regarding claim 7, Kunishima discloses the claimed invention except the choke coil.

Aioki et al. disclose an attenuator circuit including choke coil (L3), a plurality of resistors (R1-R6), a plurality of capacitors (C2-C6), and a transistor (T1).

Accordingly, it would have been obvious in view of the reference, taken as a whole, to have modified the attenuator circuit of Kunishima to have included an attenuator circuit as taught by Aioki et al. since these two circuits are equivalent. As such, replacing one for another would be within the level of ordinary skill in the art.

Regarding claim 10, wherein PIN diode (D4) is connected to an emitter of the transistor (T1) via coil (L3).

Regarding claim 11, wherein base of transistor (T1) is connected a control voltage via terminal (6).

Regarding claims 8, 9, Kunishima discloses the claimed invention except the connection of PIN diode.

Aioki et al. disclose an attenuator circuit including PIN diode, wherein PIN diode (D4) having cathode and anode connected as claimed.

Accordingly, it would have been obvious in view of the reference, taken as a whole, to have modified the attenuator circuit of Kunishima to have included an attenuator circuit as taught by Aioki et al. since these two circuits are equivalent. As such, replacing one for another would be within the level of ordinary skill in the art.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The additional references (Kam (4,019,160); Matsuura (4,668,882)) disclose variable attenuator circuit.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh V. Nguyen whose telephone number is (571) 272-1767. The examiner can normally be reached from 8:00 AM - 3:30 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

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KHANH VAN NGUYEN PRIMARY EXAMINER

Khaullandguy Dr

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